

SATURATION AND ROUNDING IN  
MULTIPLY-ACCUMULATE BLOCKS

5    Abstract of the Invention

      [0069]     Saturation and rounding capabilities are  
implemented in MAC blocks to provide rounded and  
saturated outputs of multipliers and of add-subtract-  
accumulate circuits implemented using DSP. These  
10    features support any suitable format of value  
representation, including the x.15 format. Circuitry  
within the multipliers and the add-subtract-accumulate  
circuits implement the rounding and saturation features  
of the present invention.

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